

Figure 1

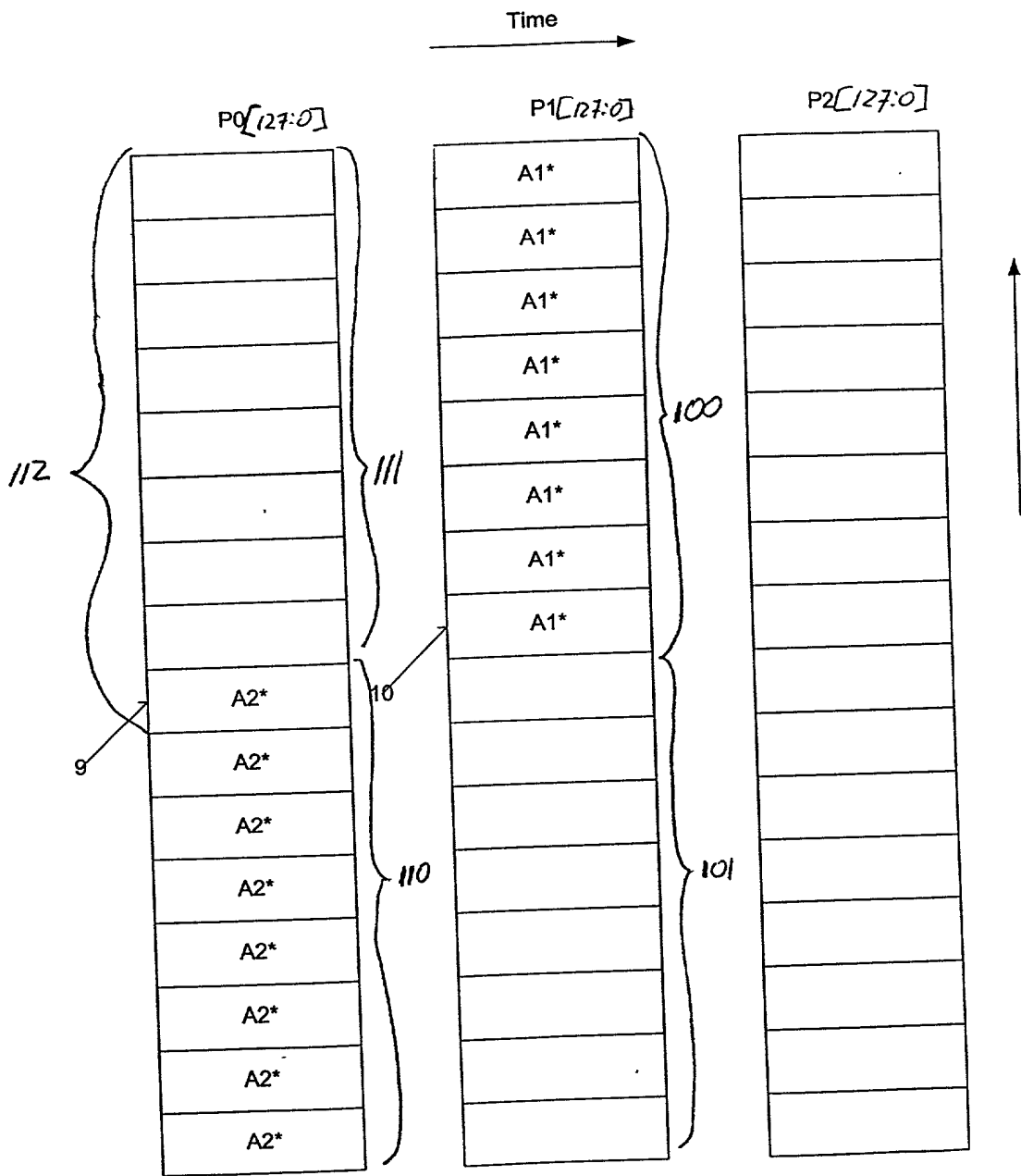


Figure 2

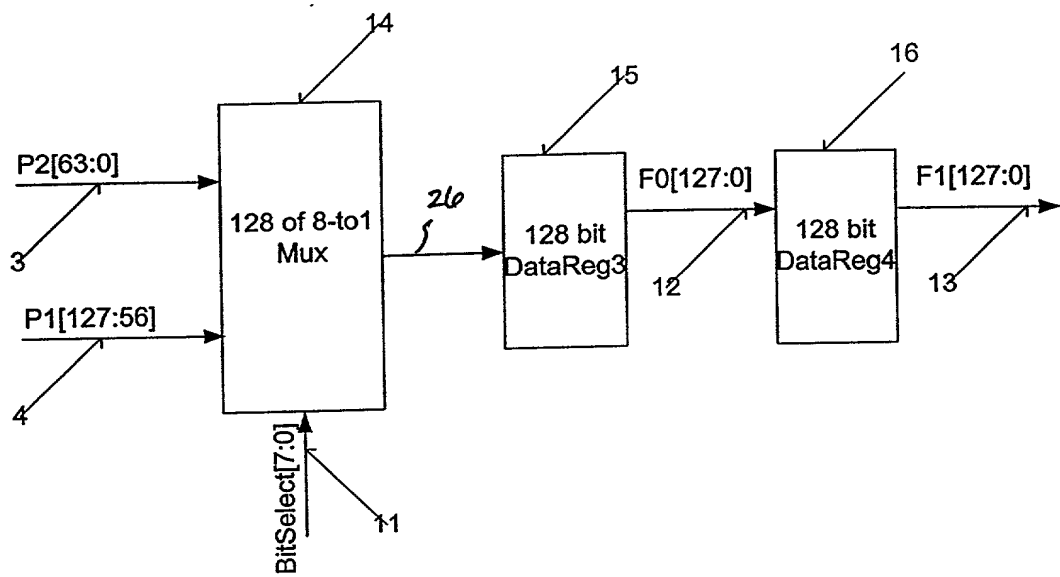


Figure 3

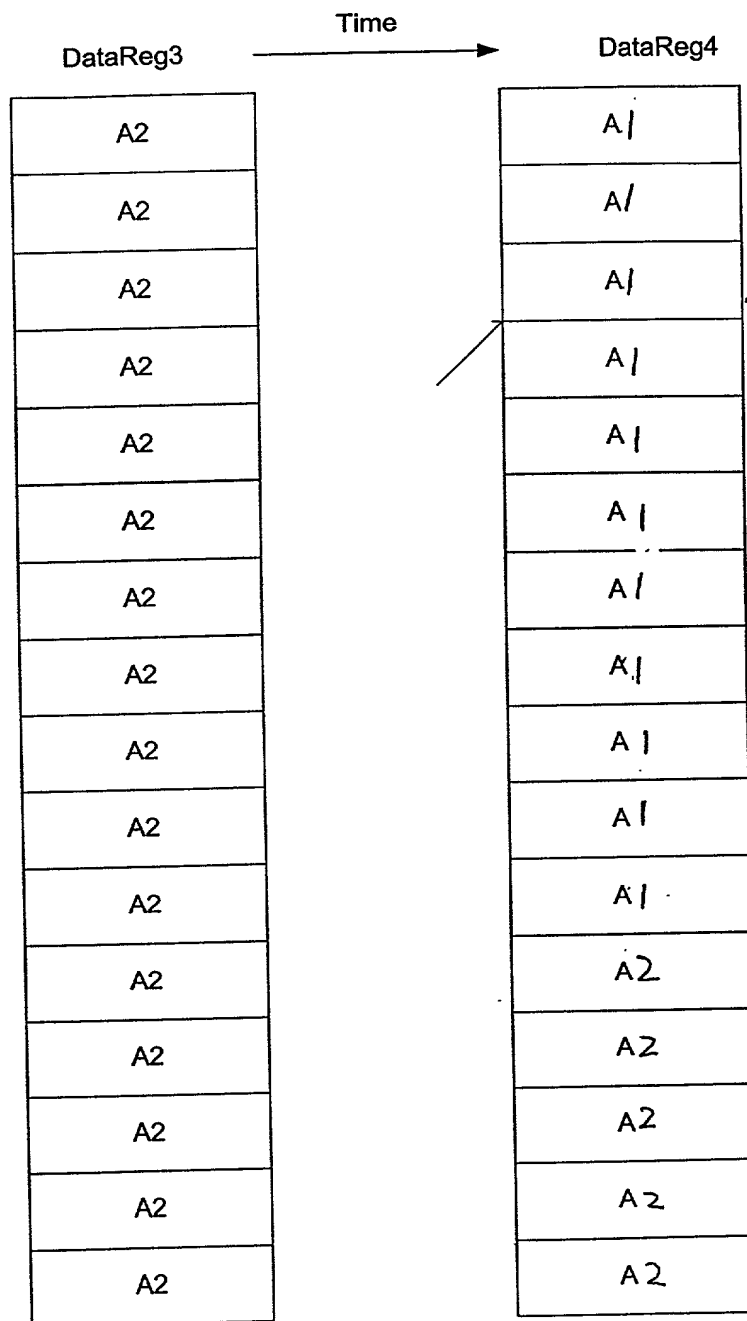


Figure 4

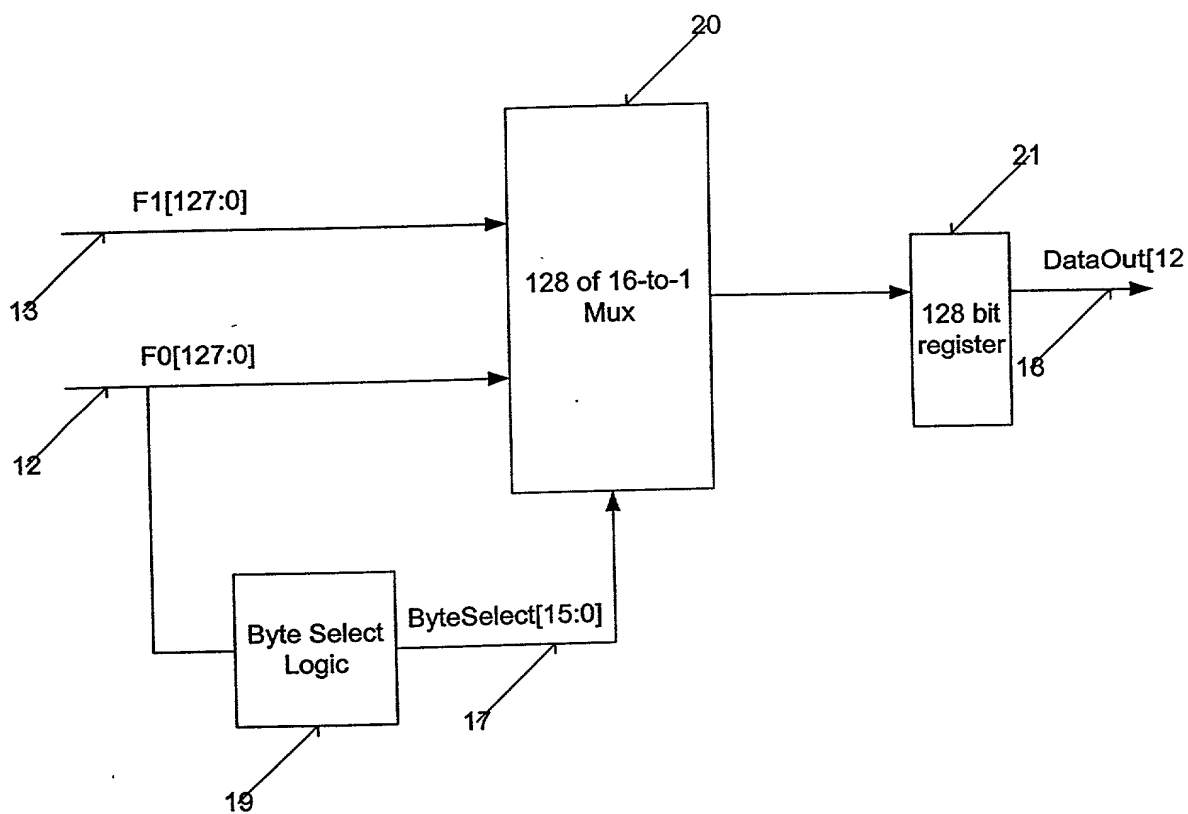


Figure 5

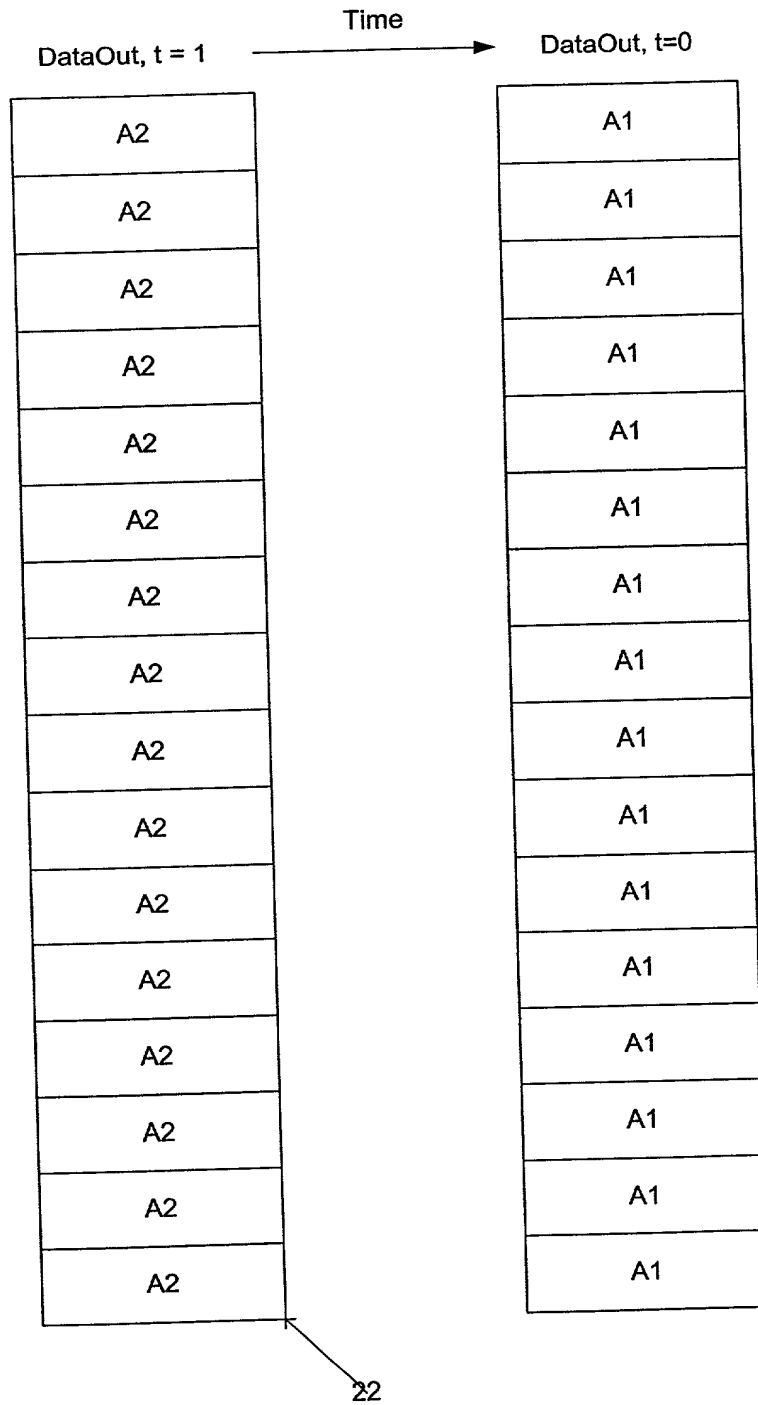


Figure 6

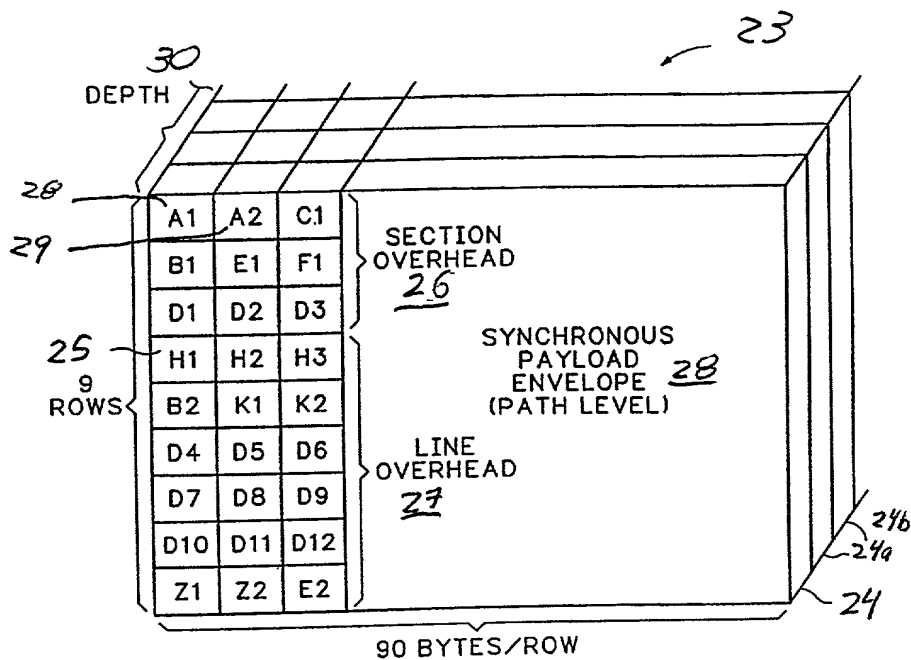


Fig. 7

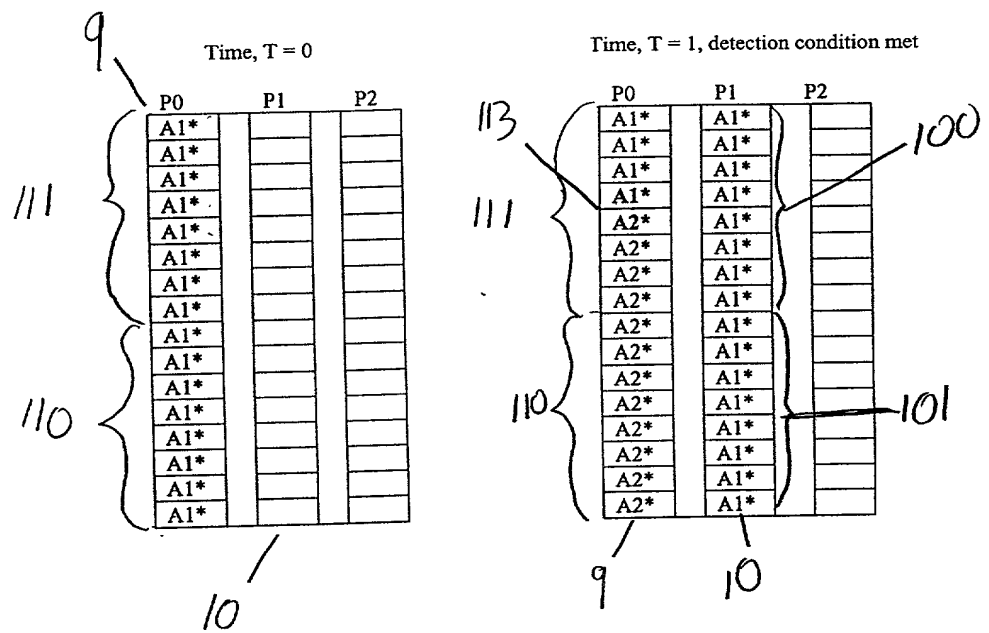


Figure 8
Detection when A1A2 Boundary is among the first 64 bits of the data bus

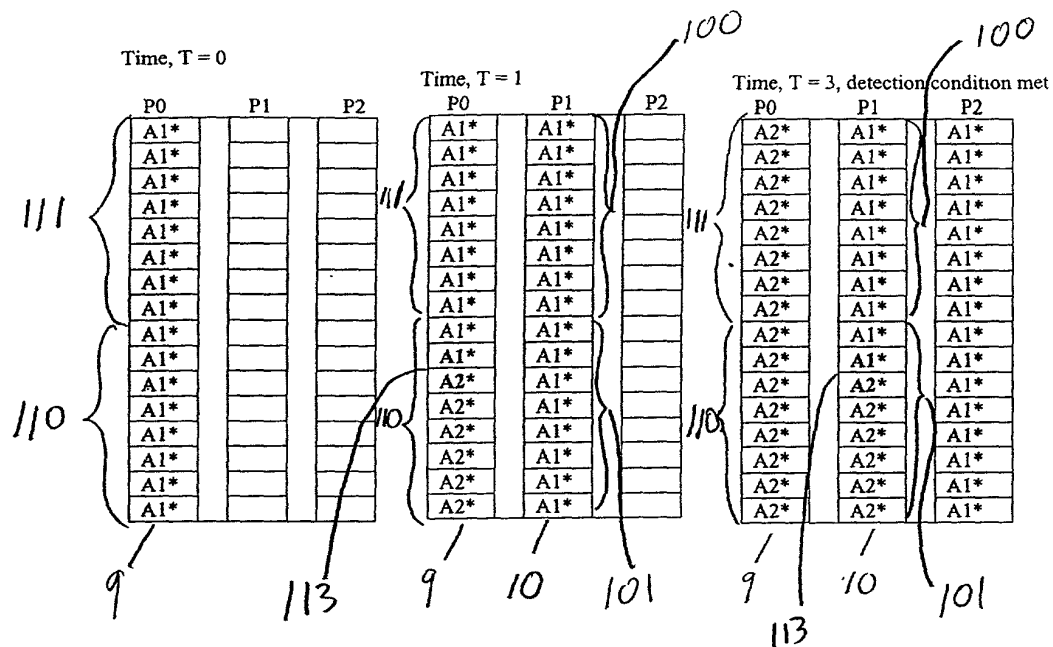
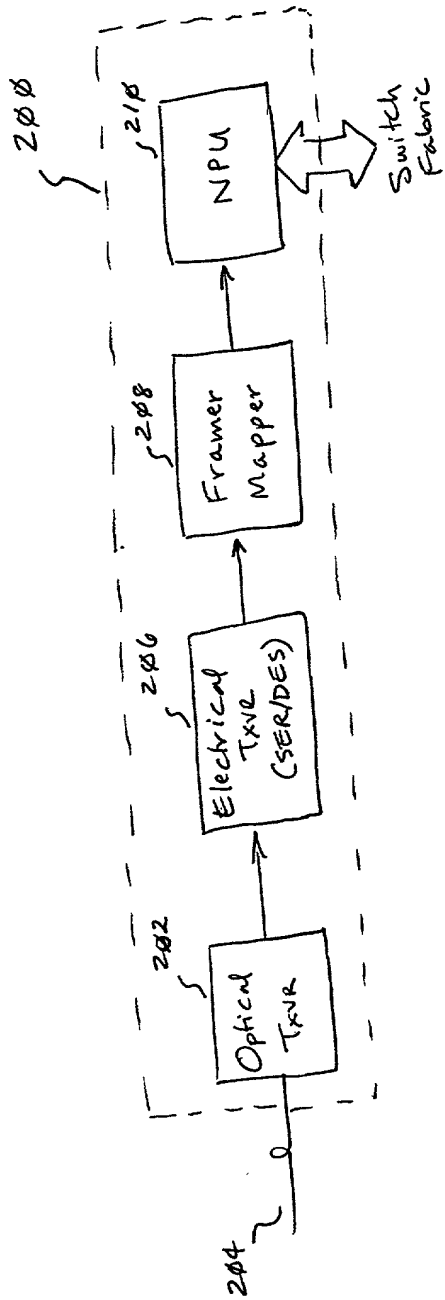


Figure 9
Detection when A1A2 Boundary is among the last 64 bits of the data bus



- FIGURE 10 -